

REGISTRATION FORM

TEQIP sponsored Short Term Training Programme on

Advanced Optimization and Applications

August 03-07, 2015

Organized by
Department of Electrical Engineering
National Institute of Technology, Raipur, C.G.

Full Name (Block Letter): _____
Name of College/Institution/Organization: _____
Qualification: _____
Designation: _____
Address: _____
Pin code: _____
Phone No.: _____
Mobile No.: _____
E-mail: _____
Do you need accommodation: (Yes/ No):

I have enclosed the registration fee of
Rs. _____ by D.D. No. _____
Dated _____ in favour of "Director, NIT,
Raipur" payable at SBI, GCET Branch, Raipur.
Place: _____
Date: _____

Signature of the participant

Duly filled registration form must reach to Department of
Electrical Engineering, NIT-Raipur before last date of
receipt of registration form by post/by hand along with
registration fee (D.D.).

Chief Patron
Prof. Sudarshan Tiwari
Director, NIT Raipur

Patron
Dr. S. Sanyal
Dean (R&C), NIT Raipur

Chairman
Dr. S. Ghosh
HoD and Assistant Professor, Department of Electrical
Engg., NIT Raipur

Co-Chairman
Dr. S. Gupta
Professor, Department of Electrical Engg., NIT Raipur

Course Coordinators
Dr. V. P. Singh, Asstt. Prof., NIT Raipur
Mr. R. K. Chaurasiya, Asstt. Prof., NIT Raipur

Organizing Committee
Dr. A. S. Thoke, NIT Raipur.
Mr. P. D. Dewangan, NIT Raipur.
Dr. S. Patnaik, NIT Raipur.
Dr. N. D. Londhe, NIT Raipur.
Dr. Anamika Yadav, NIT Raipur.
Mrs. Varsha Singh, NIT Raipur.
Dr. Ebha Koley, NIT Raipur.
Dr. Monalisa Biswal, NIT Raipur.
Mr. Lalit Kumar, NIT Raipur.
Dr. K. Chandrasekaran, NIT Raipur.

Registration Fee

Students	Rs. 1000/-
Faculty	Rs. 3000/-
Industry delegates	Rs. 5000/-

Note:

- (i) Only Breakfast, Lunch and Tea will be provided.
- (ii) Fee is non-refundable.

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Short Term Training Programme
(STTP)

on

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Mr. R. K. Chaurasiya



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Department of Electrical Engineering,
National Institute of Technology,
Raipur – 492010, Chhattisgarh.

About Institute

National Institute of Technology, Raipur (Formerly Government Engineering College Raipur), situated in the capital of a newly incepted state of Chhattisgarh, has proven to be avant-grade in the field of science and technology over past few decades in this region.



With sweet memory of foundation ceremony by our president Hon'ble Dr. Rajendra Prasad on 14th September 1956. The institute started with two departments namely Metallurgical and Mining Engineering. Later the inauguration of the institute building was done by our Prime Minister Hon'ble Pt. Jawahar Lal Nehru on 14th March 1963. From 1st December 2005, the institute has become the National Institute of Technology. It is well connected with Mumbai, Delhi, Hyderabad, Kolkata, Bhopal, Ahmedabad, Bangalore, Chennai and Chandigarh by regular flights and railway routes.

About Department

The **Department of Electrical Engineering** came into existence in 1958. Electrical Engineering department aims at imparting the state of the art knowledge and skills to the students. The department provides an outstanding research environment and offers academic program leading to the award of B.Tech, M.Tech. and Ph.D. degree.

Introduction

The short term training course is aimed at imparting knowledge and training on optimization techniques and their applications in various fields of Engineering. Expert lectures will be delivered by faculty and scientists from academia and industry. This programme will be beneficial to students (UG/PG), young researchers, research scholars, faculty who intend to practice/begin research and employ modern optimization techniques in the field of Electrical Engineering, Mechanical Engineering, Electronics Engineering, Computer Science Engineering, Information Technology, etc.

Distinguishing features of this programme include training sessions on optimization techniques. The Short term course is proposed to give a thorough exposure to the recent research trends, techniques in the field of optimization.

Objective

The objective of this programme is to train the faculty members, research scholars, young scientist and post graduate students of various organizations. The objective of the programme is also to provide a platform for interaction and exchange of ideas on recent trends in the field of optimization and its applications in various fields of engineering.

Theme

The goal of this programme is to bring together the scientist, research scholars, and students from various universities, research institutes and industry from all around the nation to exchange the new ideas face to face to establish research relations and to find opportunities for future collaboration.

Course Outline

The STTP will cover the basics as well as most recent cutting edge research on broad range of topics in the area of optimization.

The course will include basics of optimization, constrained and un-constrained optimization, local and global optimization, swarm and evolutionary optimization, optimization techniques and their basic, modified and hybrid forms and applications to different areas of science and engineering.

Eligibility

Participation in this Short Term Training Programme (STTP) is open to the under-graduate, post- graduate students, faculty members, engineers, researchers, scientists, managers and other executives working on diverse fields in Electronics Engineering, Computer Engineering, Electrical Engineering, Instrumentation Engineering, Biomedical Engineering, Biotechnology, Civil Engineering, and Mechanical Engineering.

Course Contents

- Un-constrained and constrained optimization.
- Local optimization procedures.
- Global optimization procedures.
- Multi-objective optimization.
- Evolutionary optimization techniques.
- Swarm based optimization techniques.
- Hybrid optimization techniques.
- Optimization based clustering and classification techniques.
- Applications of optimization techniques in electrical engineering, mechanical engineering, computer science, etc.

Selection

Since the number of seats is limited to 30, the selection will be made on first cum first basis and intimation will be sent to the candidates by email as per the schedule. Certificates will be issued to the participants only after attending the complete course. No TA/DA will be paid to the participants.

Accommodation may be provided on availability on payment basis at institute guest house and relevant hostels of NIT, Raipur provided request for the same is made in the registration form.

Important Dates

Last date of receipt of application: 25.07.2015,
Notification of selection (by Email): 26.07.2015.

Venue

E-Class Room and Deptt. of Electrical Engg., NIT Raipur (CG) – 492 010, INDIA.

Contact

Dr. V. P. Singh,
Assistant Professor,
Department of Electrical Engineering,
National Institute of Technology,
GE Road, Raipur-492010, India.

Contact No.: +91-8878208323, +91-8358954839.
Email: vpsingh.ele@nitrr.ac.in.